

Abstract

A method is described for monitoring the operating readiness of memory elements (18, 20) which are assigned to an
5 electronic unit (10), an engine control unit (10) for example. Furthermore, an electronic unit (10) for executing the method and a computer program, as well as a computer program product are described. In the method described, a
10 supply voltage of the electronic unit (10) is monitored to ensure error-free operation of the memory elements (18, 20).

(Figure 1)